# Product specification

## DEC10.1W Piezoelectric Amplifier Specification

The **DEC10.1W** is a piezoelectric ceramic driver primarily designed for static or low-dynamic applications. It can drive products such as piezoelectric ceramic elements, low-voltage stacked piezoelectric ceramics, and nanopositioning stages. All power supply, input, and output connections are led out via 5-pin solder pins located at the bottom, offering good solderability for easy integration into the main PCB circuit.

**DEC10.1W**

## Product Features

**1 、 1-channel compact integrated design for driving piezoelectric ceramics or capacitive loads**

**2、15V power supply**

**3、Peak current: 50mA**

**4、Average current: 10mA**

**5、No-load bandwidth: 1kHz (-3dB)**

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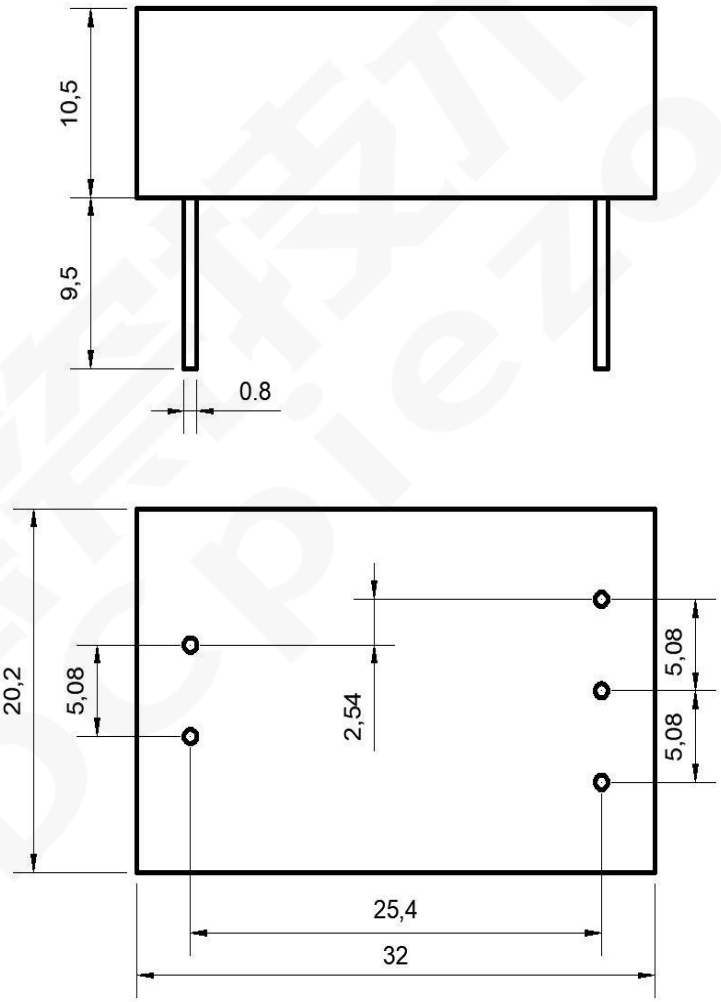
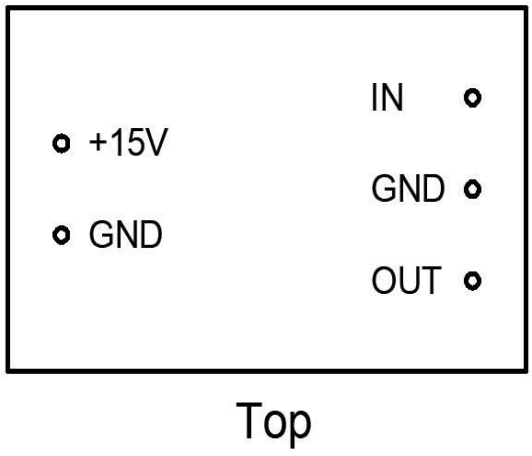
## Technical Specifications and Pin Assignment

| Piezoelectric Amplifier Parameters | | DEC10.1W |
|---|---|---|
| Power Supply | Analog Input Range | 0~10V |
| | Output Voltage Range | 0～150V |
| | Peak Current | 50mA |
| Drive (Per Channel) | Average Current | 10mA |
| | Quiescent Power Consumption | 0.2W |
| | Amplifier Bandwidth (-3 dB) | 1kHz |
| | Output Ripple Voltage | <2mV$_{RMS}$(with 1.8uF load) |
| | Supply Voltage | +15VDC/0.2A |
| | Operating Temperature Range | -20～50 ℃ |
| Dimensions | Dimensions (L × W × H) | 32×20×10.5mm |
| | Weight | 16g |
| | Interface | 5Pin |

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## Precautions

1、No heat dissipation is required for static operation. When operating under load at high frequencies, appropriate heat dissipation measures must be taken.

2、The maximum output voltage is affected by the output current; that is, as the output current increases, the maximum voltage that the driver can output decreases. This characteristic limits the maximum output voltage during dynamic operation, but has no effect on static operation.

3、The negative terminal of the analog input signal should be connected to the root of the GND pin to avoid interference.